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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/806,752	03/22/2004	Tushar Gheewala	022023-2.00US	2426
20350	7590	05/26/2005	EXAMINER	
TOWNSEND AND TOWNSEND AND CREW, LLP TWO EMBARCADERO CENTER EIGHTH FLOOR SAN FRANCISCO, CA 94111-3834			TAN, VIBOL	
		ART UNIT	PAPER NUMBER	
		2819		

DATE MAILED: 05/26/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	10/806,752	GHEEWALA, TUSHAR
Examiner	Art Unit	
Vibol Tan	2819	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
  - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1)  Responsive to communication(s) filed on 22 March 2004.

2a)  This action is **FINAL**.                            2b)  This action is non-final.

3)  Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## **Disposition of Claims**

- 4)  Claim(s) 1-19 is/are pending in the application.  
4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5)  Claim(s) 19 is/are allowed.

6)  Claim(s) 1-4 and 13-18 is/are rejected.

7)  Claim(s) 5-15 is/are objected to.

8)  Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9)  The specification is objected to by the Examiner.

10)  The drawing(s) filed on \_\_\_\_\_ is/are: a)  accepted or b)  objected to by the Examiner.

    Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

    Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)  The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12)  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a)  All b)  Some \* c)  None of:  
1.  Certified copies of the priority documents have been received.  
2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3.  Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1)  Notice of References Cited (PTO-892)  
2)  Notice of Draftsperson's Patent Drawing Review (PTO-948)  
3)  Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 3/22/04.

4)  Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_ .  
5)  Notice of Informal Patent Application (PTO-152)  
6)  Other: \_\_\_\_ .

## DETAILED ACTION

### ***Claim Rejections - 35 USC § 102***

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claim1-4, 17 and 18 are rejected under 35 U.S.C. 102(b) as being anticipated by Bertin et al. (U. S. PAT. 5,798,282).

In claim 1, Bertin et al. teaches all claimed features in Fig. 1, a hybrid circuit comprising a substrate (14); interconnects (26, 28) on said substrate; a first IC die (16) with electrical functionality (LOGIC), said first IC die being attached to said substrate (16) and having input and output signal leads (18) connected to the said interconnects via bonding mechanisms (22); and programmable logic (24) on said IC die, said programmable logic operative to intercept selected ones of communication signals (signals between 16 and 14) between said IC die and said substrate.

In claims 2-4, Bertin et al. teaches the hybrid circuit according to claim 1 wherein said substrate includes active devices (16 and 24); wherein said substrate includes programmable logic (24; col. 6, line 28); and wherein said programmable logic (24) is disposed to intercept said interconnects (28).

Claim 17 corresponds to detailed circuitry already discussed similarly with regard to claim 1.

Method claim 18 corresponds to detailed circuitry already discussed similarly with regard to claim 1.

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claims 13-16 are rejected under 35 U.S.C. 102(e) as being anticipated by Carlson (U. S. PAT. 6,861,865).

In claims 13 and 14, Carlson teaches all claimed features in Fig. 2b, a hybrid circuit comprising: a substrate (inherent, not shown); interconnects (busses) on said substrate; an IC die on said substrate (not shown), said IC die having at least two functional units (200, 201) and control logic (230) connected to output terminals (through 292, 293) of said functional units and connected to input/output terminals (282, 283) of said IC die; and said control logic (230) being operative to connect outputs of one selected functional unit to selected input/output terminals of the IC die; and wherein said two functional units are of identical functionality (200 and 201 are identical logics).

In claims 15 and 16, Carlson teaches all claimed features in Fig. 2b, an integrated circuit (IC) die for use in a hybrid circuit, said IC comprising: first and second functional electrical units (200, 201); multiplexer circuits (292, 293) connected to outputs of said first and second functional units and to input/output terminals (282, 283) the IC

die; and a control logic (230) capable of connecting outputs of a selected functional unit to selected input/output ports of the IC die; wherein said first and second functional units are of identical functionality (200 and 201 are identical logics).

5. Claims 5-12 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

6. Claim 19 appears to comprise allowable subject matter.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Vibol Tan whose telephone number is (571) 272-1811. The examiner can normally be reached on Monday-Friday (7:00 AM-4:30 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mike J. Tokar can be reached on (571) 272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



VIBOL TAN  
PRIMARY EXAMINER